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C1
wherein said first printed-circuit board mounts circuits which have a first noise resistance property, and a circuit which holds parameters unique to said disk enclosure;

wherein said second printed-circuit board mounts circuits which have a second noise resistance property which is superior to said first noise resistance property,

B1
wherein said circuits on said second printed-circuit board include a switch for selecting either of one of said first printed-circuit board connected to said second printed-circuit board and another of said first printed-circuit board connected to said second printed-circuit board, and

wherein said second printed circuit board is separated from an upper system in structure and comprises an interface control circuit that interfaces with the upper system.

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B2
12. (Twice Amended) The magnetic disk apparatus of claim 1, wherein said circuits on said second printed-circuit board are separated into a third printed circuit board and a fourth printed circuit;

wherein said third printed circuit board mounts said interface control circuit; and

wherein said fourth printed circuit board mounts said circuits other than said interface control circuit.

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B3
14. (Twice Amended) The magnetic disk apparatus of claim 1, wherein said circuits on said second printed-circuit board comprise a processor.

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15. (Twice Amended) The magnetic disk apparatus of claim 1, wherein said circuits on said second printed-circuit board comprise a spindle motor/voice coil motor control circuit.

16. (Amended) A magnetic disk apparatus comprising:

- a disk enclosure;
- a first printed-circuit board which is paired with said disk enclosure; and
- a second printed-circuit board which is connected to said first printed circuit board via a cable and is separated in structure from said first printed-circuit board,

wherein said first printed-circuit board mounts circuits having a first noise resistance property, and a circuit which holds parameters unique to said disk enclosure,

wherein said second printed circuit board mounts circuits which have a second noise resistance property which is superior to said first noise resistance property,

wherein said second printed-circuit board is separated into a third printed circuit board and a fourth printed circuit board in structure, and wherein said third printed circuit board is separated from an upper system in structure and mounts an interface control circuit that interfaces with the upper system, and

wherein said fourth printed circuit board is separated from the upper system in structure and mounts said circuits other than said interface control circuit.